IXD9205/06

600 mA Step-Down DC/DC Converter with Built-in Inductor

FEATURES

- **Built-in inductor and transistors**
- **Operating Input Voltage Range**: 2.0 V ~ 6.0 V (A/B/C types) or 1.8 V ~ 6.0 V (G type)
- **Output Voltage Range**: 0.8 V ~ 4.0 V
- **Output Voltage Error**: ±2%
- **Output Current**: 600 mA
- **High Efficiency**: 90% (V_{IN} = 4.2 V, V_{OUT} = 3.3V)
- **Oscillation Frequency**: 3 MHz
- **Maximum Duty Cycle**: 100%
- **Operating Modes**: PWM, PWM/PFM auto select
- **Functions:** Built-in Current Limit, High Speed Load Capacitor Discharge, Soft start
- **Operating Ambient temperature:** -40 ~ +85^{0}C
- **Package Size: 2.5 x 2.0 x 1.0 mm**
- **EU RoHS Compliant, Pb Free**

APPLICATION

- **Mobile Phones**
- **Bluetooth headsets**
- **Digital home appliances**
- **Office automation equipment**
- **Various portable equipment**

DESCRIPTION

The IXD9205/06 series are synchronous step-down DC/DC converters with an inductor and a control IC in one tiny (2.5 x 2.0 x 1.0 mm) package. A stable power supply with an output current of 600 mA requires only two capacitors connected externally.

Operating voltage range is from 2.0 V to 6.0 V (1.8 V ~ 6.0 V for IXD920xG version).

Output voltage is internally set in a range from 0.8 V to 4.0 V in 0.05 V increments. The device operates at 3.0 MHz switching frequency, and includes 0.42 Ω P-channel switching transistor and 0.52 Ω N-channel transistor for synchronous rectification. The IXD9205 series operate in PWM mode, while IXD9206 series automatically switching between PWM/PFM modes.

An automatic PWM/PFM switching allows fast response to the load changes, low ripple noise, and high efficiency over the full range of loads.

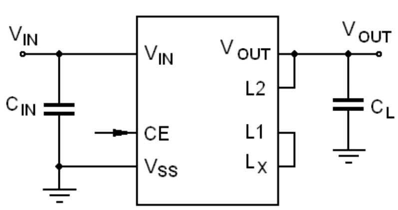
CE pin allows set device into stand-by mode with a current consumption below 1.0 µA.

The built-in Under Voltage Lockout (UVLO) function forces the internal switching transistor OFF when input voltage becomes less than 1.4V

IXD9205/06 series of B and G version have a fast soft start function internally set at 0.25 ms (typ).

IXD9205/06 series of B, C, and G version have also fast load capacitor C_L auto discharge function, which allows fast C_L discharge through switch located between the L_X and V_{SS} pins. When the devices enter stand-by mode, output voltage quickly returns to the V_{SS} level because of this function.

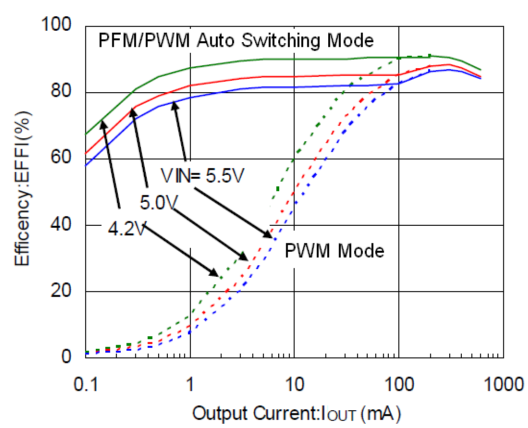
TYPICAL APPLICATION CIRCUIT



Pins L1 – L_X and L2 – V_{OUT} should be connected externally

TYPICAL PERFORMANCE CHARACTERISTIC

Efficiency vs. Output Current (f_{OSC} = 3.0 MHz, V_{OUT} = 3.3 V)



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNITS
V_{IN} Pin Voltage	V_{IN}	– 0.3 ~ 6.5	V
L_X Pin Voltage	V_{LX}	– 0.3 ~ V_{IN} + 0.3[1]	V
V_{OUT} Pin Voltage	V_{OUT}	– 0.3 ~ 6.5	V
FB Pin Voltage	V_{FB}	– 0.3 ~ 6.5	V
CE/MODE Pin Voltage	V_{CE}	– 0.3 ~ 6.5	V
Lx Pin Current	I_{LX}	±1500	mA
Inductor Current at $\Delta T = 40^0C$	I_{LMAX}	1000	mA
Power Dissipation	P_D	1000[2)]	mW
Operating Temperature Range	T_{OPR}	– 40 ~ + 85	^0C
Storage Temperature Range	T_{STG}	– 50 ~ +105	^0C

NOTE:

1. L_X pin voltage should not exceed V_{IN} +0.3 V or 6.5 V, which is less.
2. Power dissipation shown for a PCB mounted part. Please refer to page 15 for more information.

ELECTRICAL OPERATING CHARACTERISTICS

IXD9205/06 A series, Ta = 25^0C

PARAMETER		SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	CIRCUIT
Operating Voltage Range		V_{IN}		2.0	-	6.0	V	①
Output Voltage		V_{OUT}	$V_{IN} = V_{CE} = 5.0$ V, $I_{OUT} = 30$ mA	E-1	E-2	E-3	V	①
Maximum Output Current		I_{OUT_MAX}	$V_{IN} = V_{OUT(E)} + 2.0$ V, $V_{CE} = 1.0$ V[8)]	600			mA	①
UVLO Voltage		V_{UVLO}	$V_{CE} = V_{IN}$, $V_{OUT} = 0$[1), 10)]	1.00	1.40	1.78	V	③
Supply Current	IXD9205	I_Q	$V_{IN} = V_{CE} = 5.0$ V, $V_{OUT} = V_{OUT(E)}$ x 1.1 V		46	65	µA	②
	IXD9206				21	35		
Standby Current		I_{STB}	$V_{IN} = 5.0$ V, $V_{CE} = 0$ V, $V_{OUT} = V_{OUT(E)}$ x 1.1 V		0	1.0	µA	②
Oscillation Frequency		f_{OSC}	$V_{IN} = V_{OUT(E)}$ + 2 V, $V_{CE} = 1.0$ V, $I_{OUT} = 100$ mA	2550	3000	3450	kHz	①
PFM Switching Current		I_{PFM}[11)]	$V_{IN} = V_{CE} = V_{OUT(E)}$ + 2 V, , $I_{OUT} = 1$ mA	E-4	E-5	E-6	mA	⑨
P-channel ON time maximum		t_{PON_MAX}[11)]	$V_{IN} = V_{CE} = V_{OUT(T)}$ + 1 V, $I_{OUT} = 1$ mA		$2D_{max}$	$3D_{MAX}$		①
Maximum Duty Cycle Ratio		D_{MAX}	$V_{IN} = V_{CE} = 5.0$ V, $V_{OUT} = V_{OUT(E)}$ x 0.9 V	100			%	③
Minimum Duty Cycle Ratio		D_{MIN}	$V_{IN} = V_{CE} = 5.0$ V, $V_{OUT} = V_{OUT(E)}$ x 1.1 V			0	%	③
Efficiency [2)]		EFFI	$V_{IN} = V_{CE} = V_{OUT(E)}$ + 1.2 V, $I_{OUT} = 100$ mA		E-7		%	①
L_X "H" ON Resistance 1[3)]		R_{LXH1}	$V_{IN} = V_{CE} = 5.0$ V, $V_{OUT} = 0$ V, $I_{LX} = 100$ mA		0.35	0.55	Ω	④
L_X "H" ON Resistance 2[3)]		R_{LXH2}	$V_{IN} = V_{CE} = 3.6$ V, $V_{OUT} = 0$ V, $I_{LX} = 100$ mA		0.42	0.67	Ω	④
L_X "L" ON Resistance 1[4)]		R_{LXL1}	$V_{IN} = V_{CE} = 5.0$ V		0.45	0.65	Ω	
L_X "L" ON Resistance 2[4)]		R_{LXL2}	$V_{IN} = V_{CE} = 3.6$ V		0.52	0.77	Ω	
L_X "H" Leakage Current[5)]		I_{LXH}	$V_{IN} = V_{CE} = 5.0$ V, $V_{OUT} = 0$ V, $V_{LX} = 5.0$ V		0.01	1.0	µA	⑤
L_X "L" Leakage Current[5), 15)]		I_{LXH}	$V_{IN} = V_{CE} = 5.0$ V, $V_{OUT} = 0$ V, $V_{LX} = 5.0$ V		0.01	1.0	µA	⑤
Current Limit[10)]		I_{LIM}	$V_{IN} = V_{CE} = 5.0$ V, $V_{OUT} = V_{OUT(E)}$ x 0.9 V[7)]	900	1050	1350	mA	⑥
Output Voltage Temperature Characteristics		$\frac{\Delta V_{OUT}}{V_{OUT} * \Delta T_{OPR}}$	$-40^0C \le T_{OPR} \le 85^0C$, $I_{OUT} = 30$ mA		±100		ppm/^0C	①
CE "H" Voltage[13)]		V_{CEH}	$V_{OUT} = 0$ V	0.65		6.0	V	③
CE "L" Voltage[14)]		V_{CEL}	$V_{OUT} = 0$ V	0		0.25	V	③
CE "H" Current		I_{ENH}	$V_{IN} = V_{CE} = 5.0$ V, $V_{OUT} = 0$ V	-0.1		0.1	µA	⑤
CE "L" Current		I_{ENL}	$V_{IN} = 5.0$ V, $V_{CE} = 0$ V, $V_{OUT} = 0$ V	-0.1		0.1	µA	⑤
Soft-Start Time	A, C version	t_{SS}	$I_{OUT} = 1$ mA	0.5	0.9	2.5	ms	①
	B, G versions				E-11	E-12		
Latch Time[6)]		t_{LAT}	$V_{IN} = V_{CE} = 5.0$ V, $V_{OUT} = 0.8$ x $V_{OUT(E),}$ L_X short with 1 Ω resistor to ground	1.0		20.0	ms	⑦

ELECTRICAL OPERATING CHARACTERISTICS (CONTINUED)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	CIRCUIT
Short Protection Threshold Voltage	V_{SHORT}	$V_{IN} = V_{CE} = 5.0$ V, L_X short with 1 Ω resistor to ground[12)	E-8	E-9	E-10	V	⑦
C_L Discharge Resistance[13)	R_{CLD}	$V_{IN} = L_X = 5$ V, $V_{CE} = 0$, V_{OUT} = open	200	300	450	Ω	⑧
Inductance Value	L	Test frequency 1 MHz		1.5		µH	
Inductor Current Maximum	I_{LMAX}	$\Delta T = 40^0$C		1000		mA	

NOTE:
Test conditions: Unless otherwise stated, $V_{IN} = 5.0$ V, $V_{OUT\ (E)}$ = Nominal Voltage

1) Including hysteresis operating voltage range
2) EFFI = {(output voltage × output current) / (input voltage × input current)} × 100%
3) ON resistance (Ω) = (V_{IN} - Lx pin measurement voltage) / 100mA
4) Design target value
5) A 10µA (maximum) current may leak at high temperature
6) Time from moment when V_{OUT} is shorted to GND via 1 Ω resistor to the moment, when Current Limit generates pulse stopping L_X oscillations
7) When V_{IN} is less than 2.4 V, current limit may not be reached because of voltage drop across ON resistance
8) When the difference between input and output voltage is small, some cycles may be skipped completely before current maximizes. If load current increases in this state, output voltage will decrease because of the voltage drop across P-channel transistor
9) Current limit denotes the level of an inductor peak current
10) Voltage, when L_X pin voltage is "L"=+0.1 V ~ -0.1 V
11) IXD9206 series only
12) V_{OUT} voltage at which L_X pin state changes from "H" to "L" within 1 ms
13) B, C, and G versions only
14) Version A only

E-TABLES

Output Voltage Error

NOMINAL OUTPUT VOLTAGE	V_{OUT}, (V)		
	E-1	E-2	E-3
$V_{OUT(T)}$, V	MIN	TYP	MAX
1.00	0.980	1.000	1.020
1.20	1.176	1.200	1.224
1.40	1.372	1.400	1.428
1.50	1.470	1.500	1.530
1.75	1.715	1.750	1.785
1.80	1.764	1.800	1.836
1.90	1.862	1.900	1.938
2.50	2.450	2.500	2.550
2.80	2.744	2.800	2.856
2.85	2.793	2.850	2.907
3.00	2.940	3.000	3.060
3.30	3.234	3.300	3.366

PFM Switching Current

NOMINAL OUTPUT VOLTAGE	I_{PFM}, (mA)		
	E-4	E-5	E-6
	MIN	MAX	TYP
$V_{OUT(T)} < 1.2$ V	190	260	350
$1.2\ V < V_{OUT(T)} \leq 1.75$ V	180	240	300
$1.8\ V < V_{OUT(T)}$	170	220	270

Efficiency

NOMINAL OUTPUT VOLTAGE	EFFICIENCY, %
	E-7
$V_{OUT(T)}$, V	
1.00	79
1.20	82
1.40	83
1.50	84
1.75 – 1.90	85
2.50 – 3.30	86

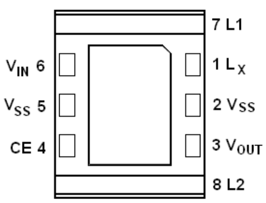
Short Circuit Protection Threshold Voltage

NOMINAL OUTPUT VOLTAGE	V_{SHORT}, (V)					
	A, B, C VERSIONS			G VERSION		
	E-8	E-9	E-10	E-8	E-9	E-10
$V_{OUT(T)}$, V	MIN	TYP	MAX	MIN	TYP	MAX
1.00	0.375	0.500	0.625	0.188	0.250	0.313
1.20	0.450	0.600	0.750	0.225	0.300	0.375
1.40	0.525	0.700	0.875	0.263	0.350	0.438
1.50	0.563	0.750	0.938	0.282	0.375	0.469
1.75	0.656	0.875	1.094	0.328	0.438	0.547
1.80	0.675	0.900	1.125	0.338	0.450	0.563
1.90	0.713	0.950	1.188	0.357	0.475	0.594
2.50	0.938	1.250	1.563	0.469	0.625	0.782
2.80	1.050	1.400	1.750	0.525	0.700	0.875
2.85	1.069	1.425	1.781	0.535	0.713	0.891
3.00	1.125	1.500	1.875	0.563	0.750	0.938
3.30	1.238	1.650	2.063	0.619	0.825	1.032

Soft-Start Time (IXD9205/06 B and G versions only)

NOMINAL OUTPUT VOLTAGE $V_{OUT(T)}$, V	t_{SS}, (ms)	
	E-11	E-12
	TYP	MAX
0.8 V < $V_{OUT(T)}$ ≤1.75 V	0.25	0.4
1.8 V < $V_{OUT(T)}$ ≤4.0 V	0.32	0.5

PIN CONFIGURATION



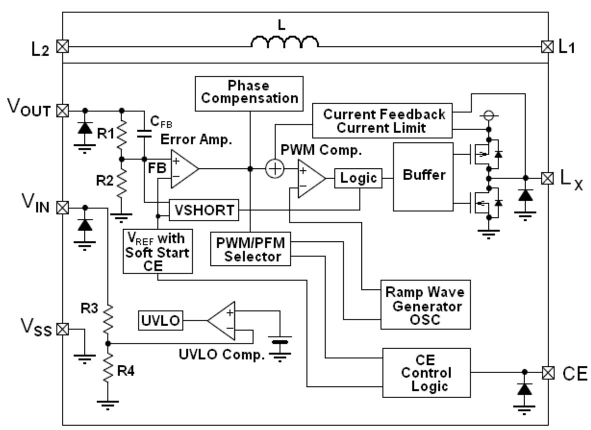
NOTE:
The dissipation pad should be soldered in recommended mount pattern and metal masking to enhance mounting strength and heat release. If the pad needs to be connected to other pins, it should be connected to the V_{SS} (No 2 and No 5) pins.
V_{SS} pins (No. 2 and 5) should be tied together and connected to ground plane on the board.

PIN ASSIGNMENT

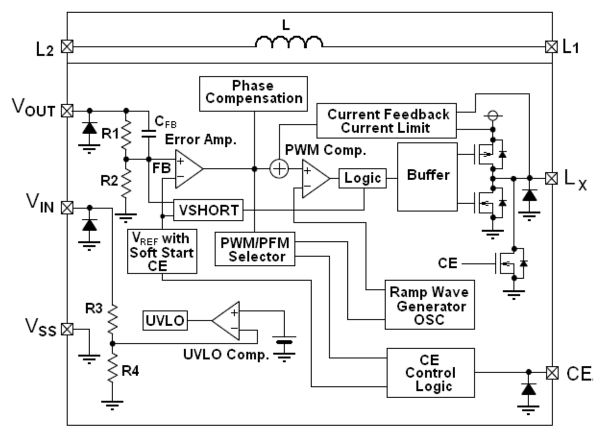
PIN NUMBER	PIN NAME	FUNCTIONS
1	L_X	Switching Node
2, 5	V_{SS}	Ground
3	V_{OUT}	Fixed Output Voltage
4	CE	Enable (Active HIGH)
6	V_{IN}	Power Input
7	L1	Inductor Connection
8	L2	Inductor Connection

BLOCK DIAGRAMS

IXD9205/06 A Series



IXD9205/06 B/C/G Series



Internal diodes include an ESD protection and a parasitic diode

BASIC OPERATION

The IXD9205/06 series consists of a Reference Voltage source, Ramp Wave Generator, Error Amplifier, PWM Comparator, Phase Compensation circuit, output voltage resistive divider, P-channel switching transistor, N-channel transistor for the synchronous switch, Current Limiter circuit, UVLO circuit, and others. (See the block diagrams above.)

The Error Amplifier compares output voltage divided by internal resistors R_1/R_2 with the internal reference voltage. Amplified difference between these two signals applies to the one input of the PWM Comparator, while ramp voltage from the Ramp Wave Generator applies to the second input. Resulting PWM pulse determines switching transistor ON time. It goes through the Buffer and it appears at the gate of the internal P-channel switching transistor. This continuous process stabilizes output voltage.

The Current Feedback circuit monitors current of the P-channel transistor at each switching cycle, and modulates output signal from the Error Amplifier to provide additional feedback. This guarantees a stable converter operation even with low ESR ceramic load capacitor.

Reference Voltage Source

The Reference Voltage Source provides the reference voltage to ensure stable output voltage of the DC/DC converter.

Ramp Wave Generator

The Ramp Wave Generator produces ramp waveform signal needed for PWM operation, and signals to synchronize all the internal circuits. It operates at internally fixed 1.2 MHz or 3.0 MHz frequency.

Error Amplifier

The Error Amplifier monitors output voltage through resistive divider connected to V_{OUT} (FB) pin. If output voltage falls below preset value and Error Amplifier's input signal becomes less than internal reference voltage, the Error Amplifier/s output signal increases. That results in wider PWM pulse and respectively longer ON time for switching transistor to increase output voltage. The gain and frequency characteristics of the error amplifier output are fixed internally to optimize IC performance.

Current Limiter

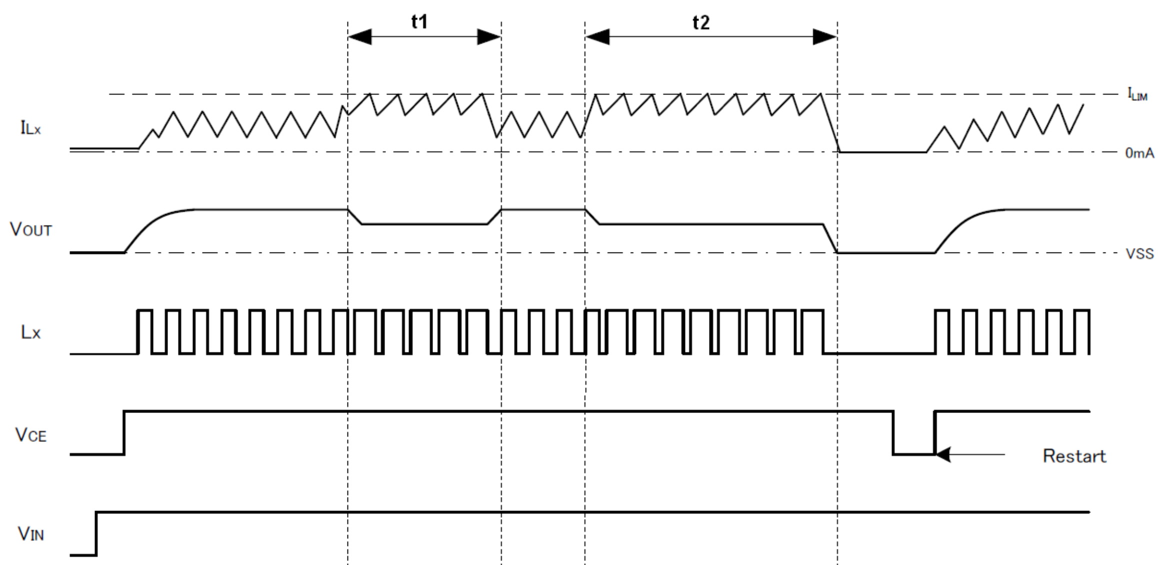
The Current Limiter circuit monitors current flowing through the P-channel transistor connected to the Lx pin, and combines function of the current limit and operation suspension.

When transistor's current is greater than a specified level, the Current Limiter turns off P-channel transistor immediately. After that, the Current Limiter turns off too, returning to monitoring mode.

The driver transistor turns on at the next cycle, but the Current Limiter will turn it off immediately, if an over- current exists. When the over current state is eliminated, the IC resumes its normal operation.

The IC waits for end of the over current state repeating above steps (t1 on figure below). If an over-current state continues for a few ms with IC repeatedly performing above steps, the Current Limiter latches the P-channel transistor in OFF state, and IC suspends operations (t2 on figure below). To restart IC operation after this condition, either CE pin should be toggled H – L – H, or V_{IN} pin voltage should be set below UVLO to resume operations from soft start.

The suspension mode is not a standby mode. In the suspension mode, pulse output is suspended; however, internal circuitries remain in operation mode consuming power.



Short-Circuit Protection

The short-circuit protection monitors the R_{FB1}/R_{FB2} divider voltage (FB point in the block diagram). If output is accidentally shorted to the ground, FB voltage starts falling. When this voltage becomes less than half of the reference voltage (V_{REF}) and P-channel switching transistor's current is more than the I_{LIM} threshold, the Short-Circuit Protection turns off and latches quickly the P-channel transistor.

At D/E/F/G series, Short Circuit Protection starts once FB voltage becomes less than 0.25 of reference voltage (V_{REF}), disregard to transistor's current.

To restart IC operation after this condition, either EN pin should be toggled H – L – H, or V_{IN} pin voltage should be set below UVLO to resume operations from soft start.

The sharp load transients creating a voltage drop at the V_{OUT}, propagate to the FB point through C_{FB}, that may result in Short Circuit protection operating at voltages higher than 1/2 V_{REF} voltage.

UVLO Circuit

When the V_{IN} pin voltage becomes 1.4V or lower, the P-channel transistor is forced OFF to prevent false pulse output caused by unstable operation of the internal circuitry. When the V_{IN} pin voltage becomes 1.8 V or higher, switching operations resume with the soft start. The soft start function operates even when the V_{IN} voltage falls below the UVLO threshold for a very short time. The UVLO circuit does not cause a complete shutdown of the IC, but causes pulse output to be suspended; therefore, the internal circuitry remains in operation.

PFM Switch Current

In PFM mode, the IC keeps the P-channel transistor on until inductor current reaches a specified level (I_{PFM}).

P-channel transistor's ON time is equal

$$t_{ON} = L \times I_{PFM} / (V_{IN} - V_{OUT}),\ \mu s,$$

where L is an inductance in µH, and I_{PFM} is a current limit in A.

PFM Duty Limit

In PFM mode, P-channel ON time maximum (t_{PON_MAX}) is set to $2D_{MAX}$, i.e. two periods of the switching frequency. Therefore, under conditions, when the ON time increases (i.e. step-down ratio is small), it is possible that P-channel transistor to be turned off, even when inductor current does not reach to I_{PFM}. (See Figures 1 and 2 below)

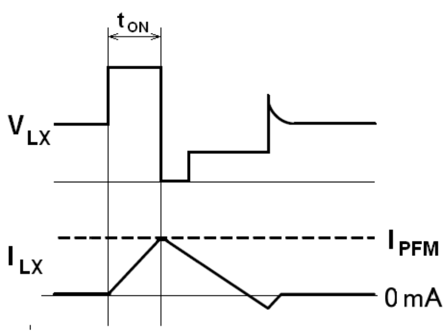


Figure 1

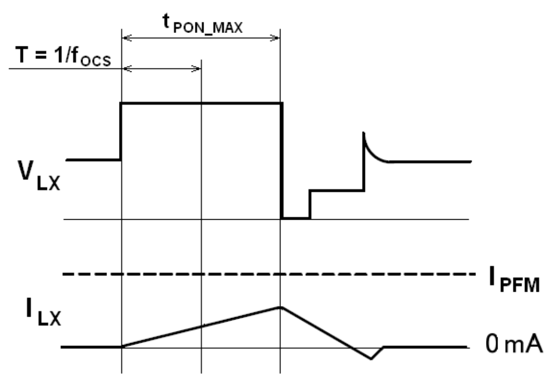


Figure 2

C_L High Speed Discharge

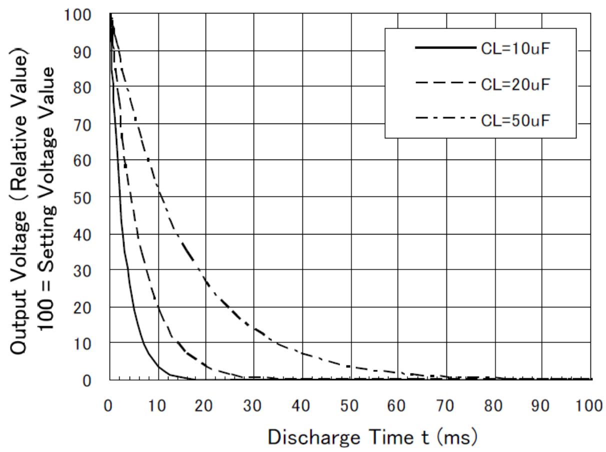
The IXD9205/06B, C, and G series can quickly discharge the output capacitor (C_L) to avoid application malfunction, when CE pin set logic LOW to disable IC.

C_L Discharge Time is proportional to the resistance (R) of the N-channel transistor located between the L_X pin and ground and the output C_L capacitance as shown below.

$$t_{DSH} = RC_L \times \mathrm{Ln}\,(V_{OUT(E)} / V),\ \text{where}$$

V - Output voltage after discharge
$V_{OUT(E)}$ - Output voltage
R = 300 Ω (Typical value)

Output Voltage Discharge Characteristics



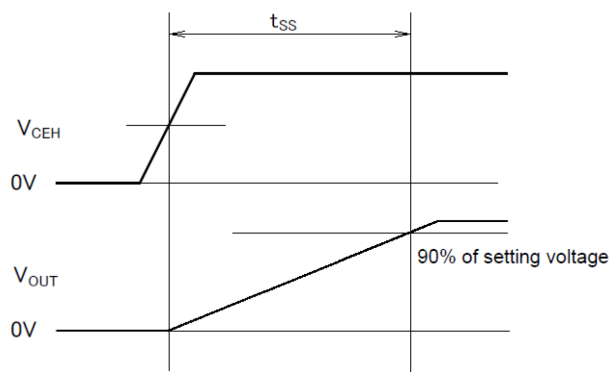
CE Pin Function

The IXD9205/06 series enter the shut down mode, when a LOW logic-level signal applies to the CE pin. In the shutdown mode, IC current consumption is ~0 μA (Typical value), with the Lx and V_{OUT} pins at high impedance state. The IC starts its operation when a HIGH logic-level signal applies to the CE.

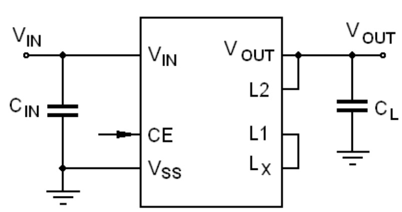
Soft Start

Soft start time is available in two options via product selection.

The soft-start time of IXD9205/06 series is optimized by using internal circuits. The definition of the soft-start time is the time when the output voltage goes up to the 90% of nominal output voltage after the IC is enabled by CE "H" signal.



TYPICAL APPLICATION CIRCUIT

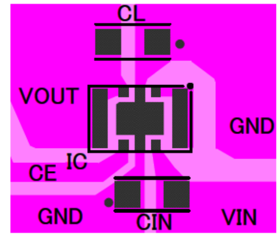


EXTERNAL COMPONENTS

f_{OSC}	3.0 MHz
C_{IN}, μF	4.7
C_L, μF	10

Capacitors should be X7R or X5R series to minimize power losses.

LAYOUT AND USE CONSIDERATIONS

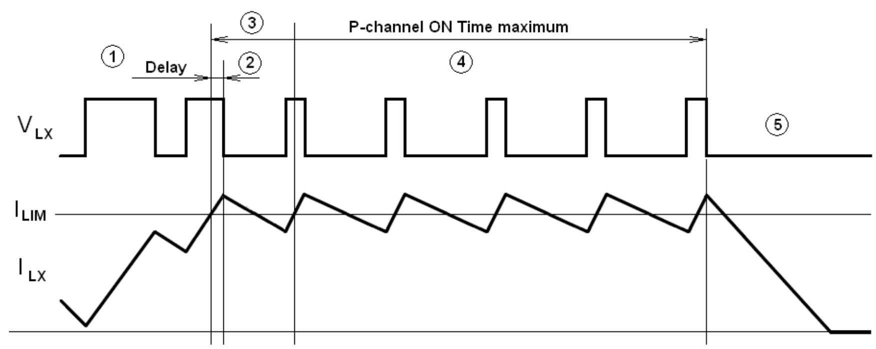


1. Wire external components as close to the IC as possible and use thick, short connecting traces to reduce the circuit impedance. Please, pay special attention to the V_{IN} and GND wiring. Switching noise, which occurs from the GND, may cause the instability of the IC, so, position V_{IN} and V_{CL} capacitors as close to IC as possible (See recommended layout on the right).
2. Transitional voltage drops or voltage rising phenomenon could make the IC unstable if ratings are exceeded.
3. The IXD9205/06 series are designed to work with ceramic output capacitors. However, if the difference between input and output voltages is too high, a ceramic capacitor may fail to absorb the resulting high switching energy and oscillation could occur. In this case, connect an electrolytic capacitor in parallel to ceramic one to compensate for insufficient capacitance.
4. In PWM mode, IC generates very narrow pulses, and there is a possibility that some cycles will be skipped completely, if the difference between V_{IN} and V_{OUT} is high.
5. If the difference between V_{IN} and V_{OUT} is small, IC generates very wide pulses, and there is a possibility that some cycles will be skipped completely at the heavy load current.
6. When dropout voltage or load current is high, Current Limit may activate prematurely that will lead to IC instability. To avoid this condition, choose inductor's value to set peak current below Current Limit threshold. Calculate the peak current according to the following formula:

$$I_{PK} = (V_{IN} - V_{OUT}) \times D / (2 \times L \times f_{OSC}) + I_{OUT},$$ where

L - Inductance
f_{OSC} -- Oscillation Frequency
D – Duty cycle

7. Inductor's rated current exceeds Current Limit threshold of 1050 mA to avoid damage, which may occur until P-channel transistor turns off after Current Limiter activates, but pulse current may exceed this



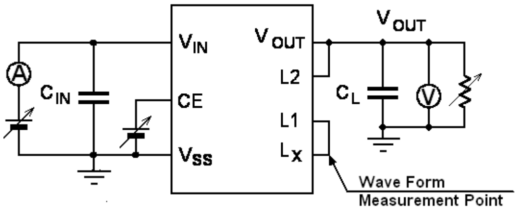
value(see figure below).

① Current flows into P-channel transistor reaches the current limit (I_{LIM}).
② Current is more than I_{LIM} due the circuit's delay time from the current limit detection to the P-channel transistor OFF.
③ The inductor's current time rate becomes quite small.
⑨ IC generates very narrow pulses for several milliseconds.
⑤ The circuit latches, stopping operation.

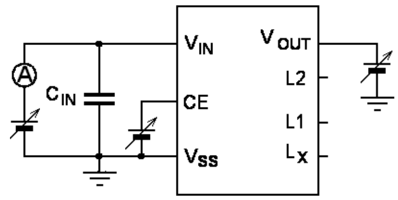
8. If V_{IN} voltage is less than 2.4 V, current limit threshold may be not reached due voltage drop caused by switching transistor's ON resistance
9. Latch time may become longer or latch may not work due electrical noise. To avoid this effect, the board should be laid out so that input capacitors are placed as close to the IC as possible.
10. Use of the IC at voltages below recommended voltage range may lead to instability.
11. At high temperature, output voltage may increase up to input voltage level at no load, because of the leakage current of the driver transistor.
12. High step-down ratio and very light load may be cause of intermittent oscillations in PWM mode.
13. In PWM/PFM automatic switching mode, IC may become unstable during transition to continuous mode. Please verify with actual components.

TEST CIRCUITS

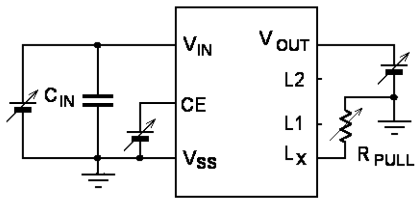
Circuit ①



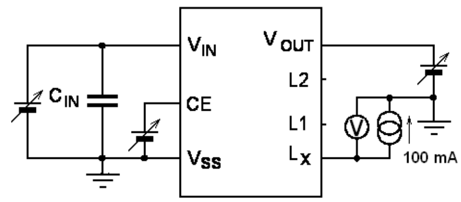
Circuit ②



Circuit ③

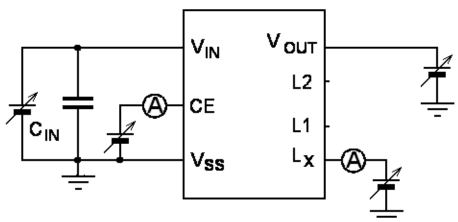


Circuit ④

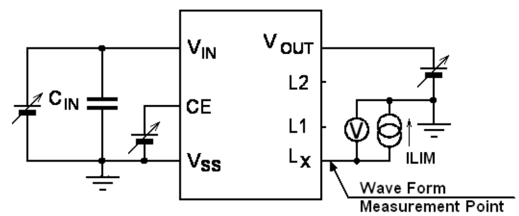


ON Resistance = (V_{IN} – V_{OUT}/100 mA

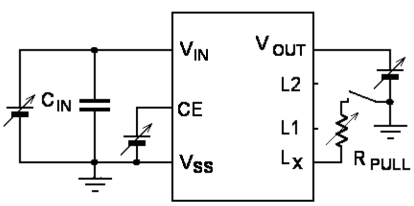
Circuit ⑤



Circuit ⑥

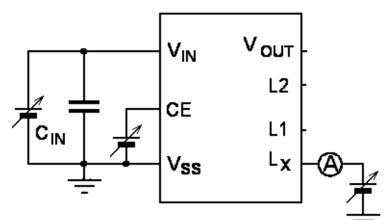


Circuit ⑦

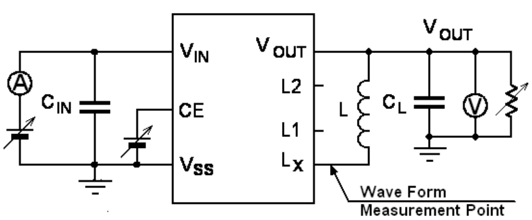


R_{PULL} = 1 Ω

Circuit ⑧



Circuit ⑨



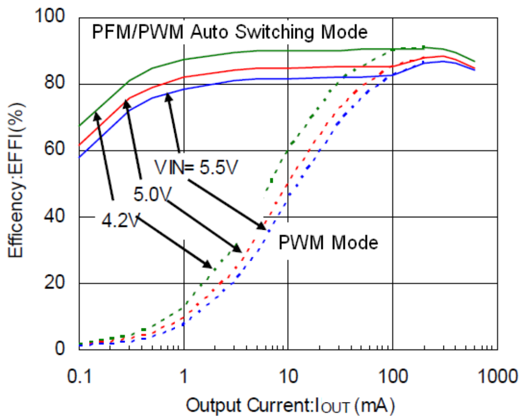
External Components
C_{IN} = 4.7 µF (ceramic), C_L = 10 µF (ceramic)
L = 1.5 µH
R_{PULL} = 200 Ω

TYPICAL PERFORMANCE CHARACTERISTICS

(1) Efficiency vs. Output Current

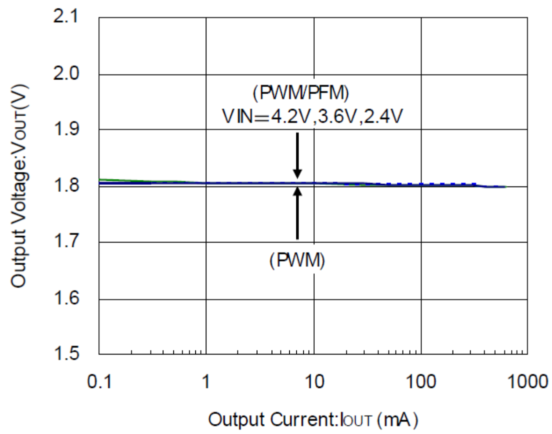
IXD920xA183AR-G

C_{IN} = 4.7 μF, C_L= 10 μF



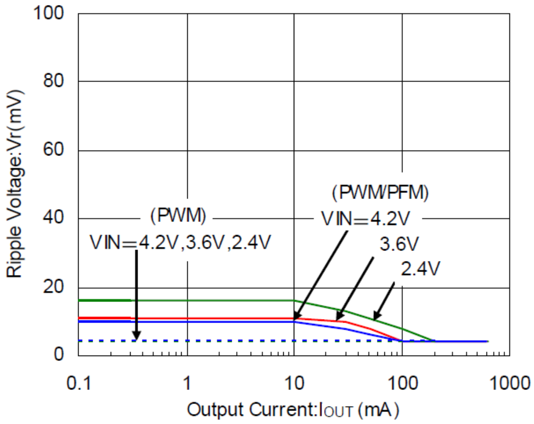
(2) Output Voltage vs. Output Current

IXD920xA183AR-G



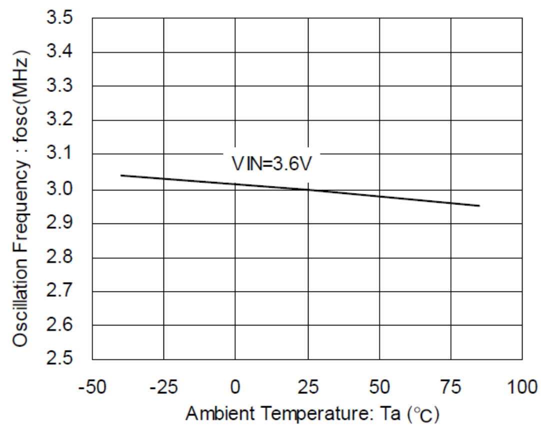
(3) Ripple Voltage vs. Output Current

IXD920xA183AR-G



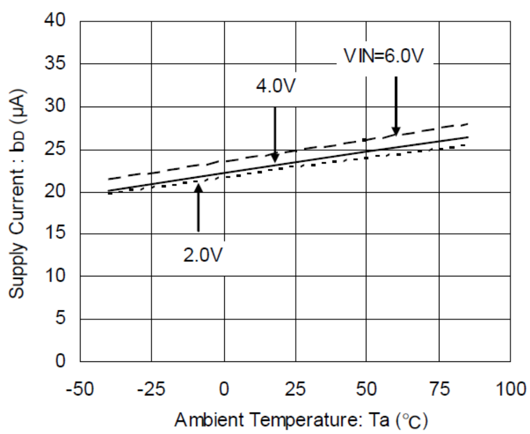
(4) Oscillation Frequency vs. Ambient Temperature

IXD920xA183AR-G



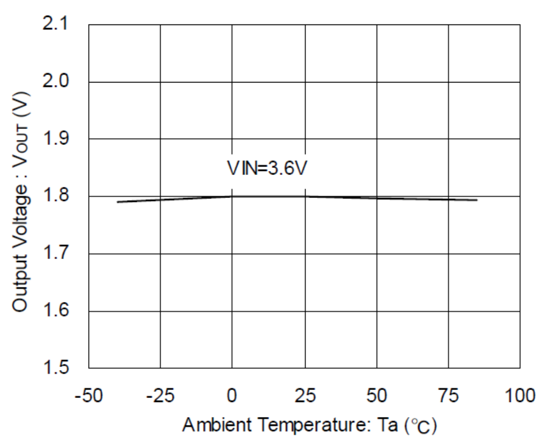
(5) Supply Current vs. Ambient Temperature

IXD920xA183AR-G



(6) Output Voltage vs. Ambient Temperature

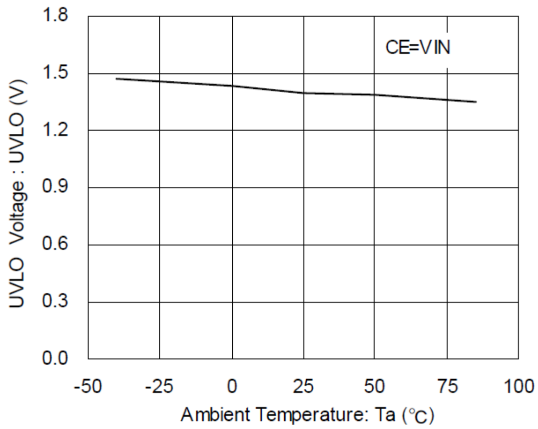
IXD920xA183AR-G



TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

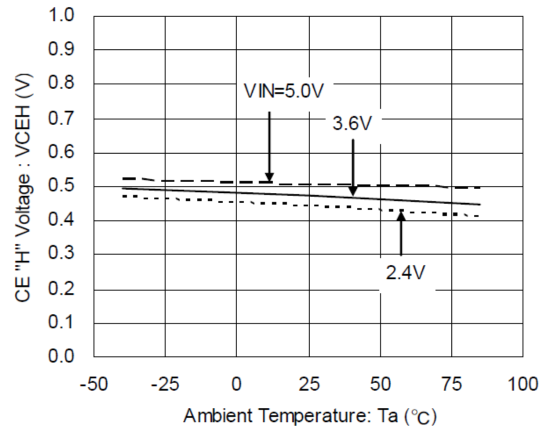
(7) UVLO Voltage vs. Ambient Temperature

IXD920xA183AR-G



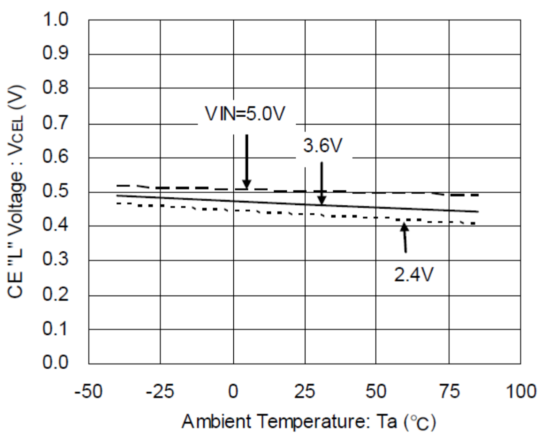
(8) CE "H" Voltage vs. Ambient Temparature

IXD920xA183AR-G



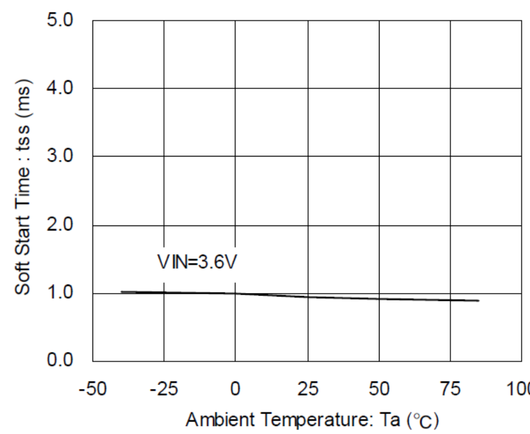
(9) CE "L" Voltage vs. Ambient Temperature

IXD920xA183AR-G



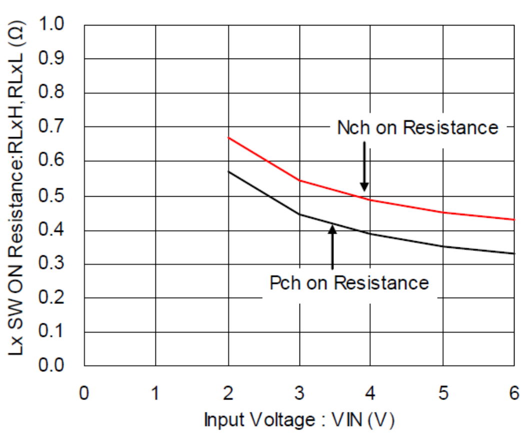
(10) Soft Start Time vs. Ambient Temperature

IXD920xA183AR-G



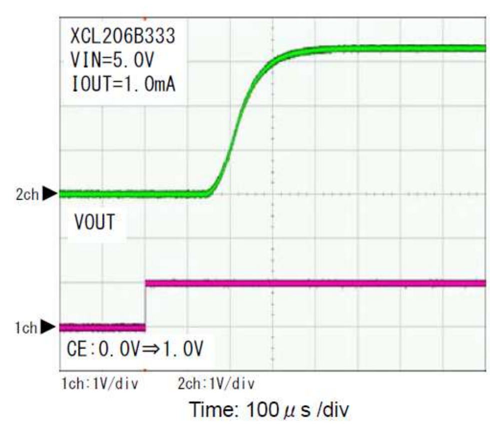
(11) ON Resistance vs. Ambient Temperature

IXD920xA183AR-G



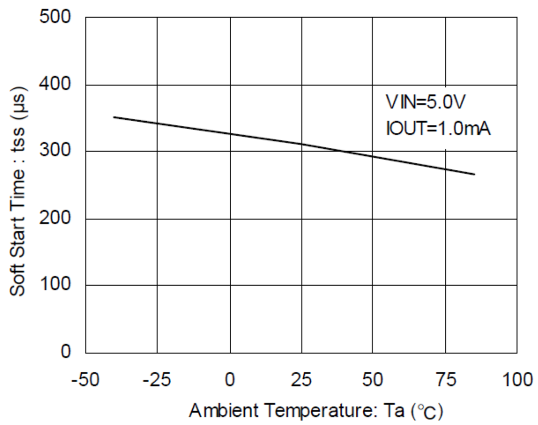
(12) Start Wave Form

IXD920xB333AR-G



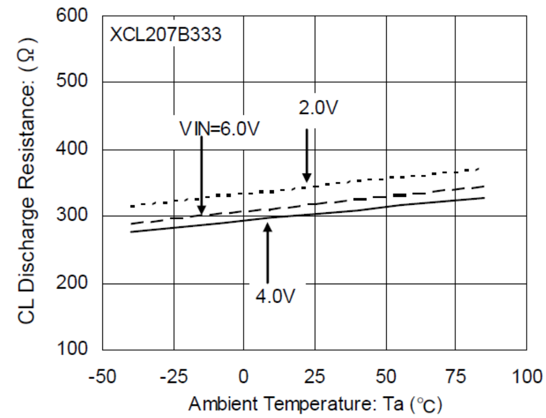
(13) Soft Start Time vs. Ambient Temperature

IXD920xB333AR-G



(14) C_L Discharge Time vs. Ambient Temperature

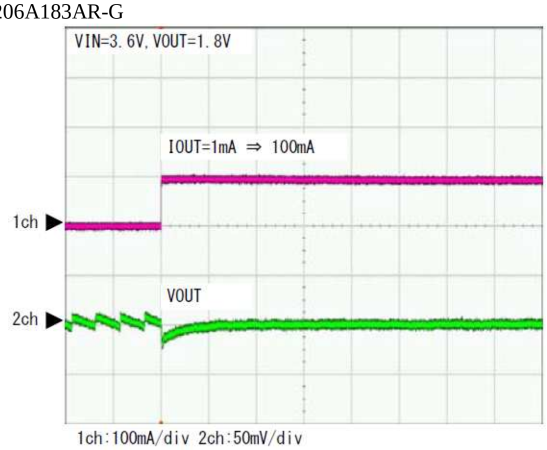
IXD920xB333AR-G



(15) Load Transient Response

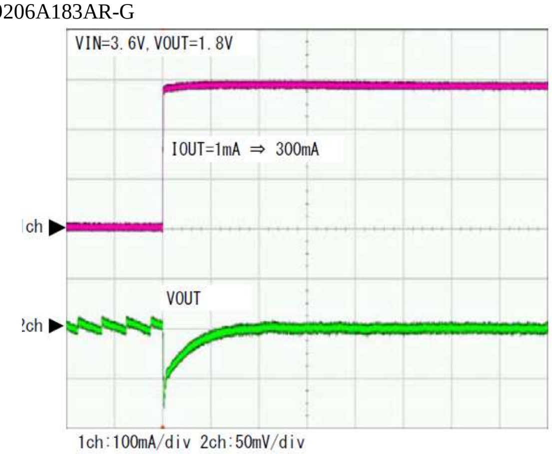
MODE: PWM/PFM Auto Switching

IXD9206A183AR-G



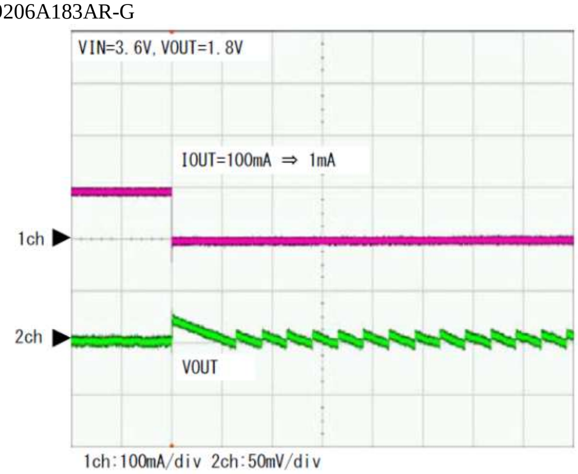
Ch1 – I_{OUT}, Ch2 – V_{OUT} 50 mV/div, Time – 100 µs/div

IXD9206A183AR-G



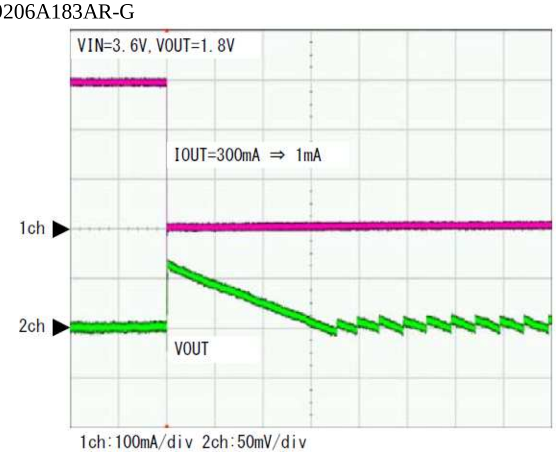
Ch1 – I_{OUT}, Ch2 – V_{OUT} 50 mV/div, Time – 100 µs/div

IXD9206A183AR-G



Ch1 – I_{OUT}, Ch2 – V_{OUT} 50 mV/div, Time – 100 µs/div

IXD9206A183AR-G



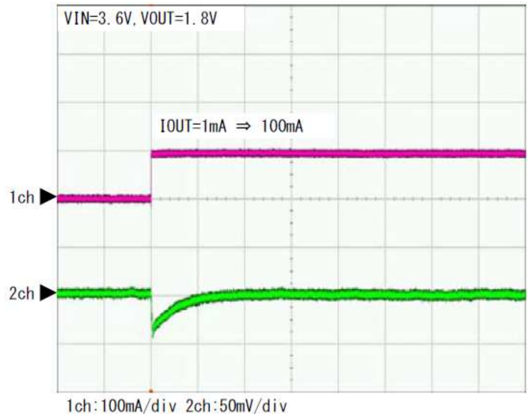
Ch1 – I_{OUT}, Ch2 – V_{OUT} 50 mV/div, Time – 100 µs/div

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(15) Load Transient Response (Continued)

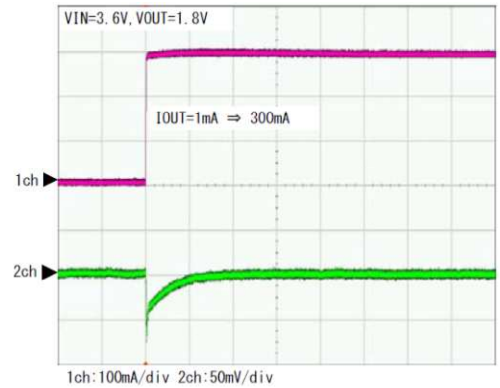
MODE: PWM

IXD9205A183AR-G



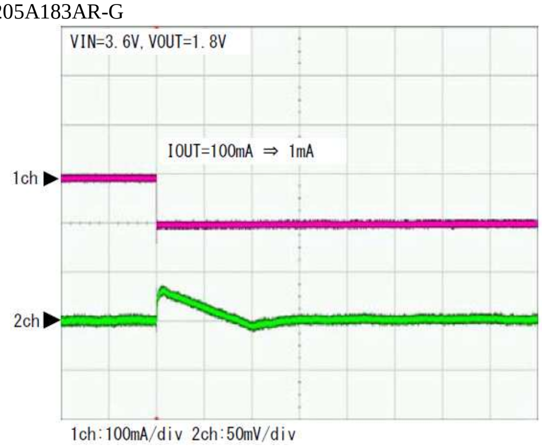
Ch1 – I_{OUT}, Ch2 – V_{OUT} 50 mV/div, Time – 100 µs/div

IXD9205A183AR-G



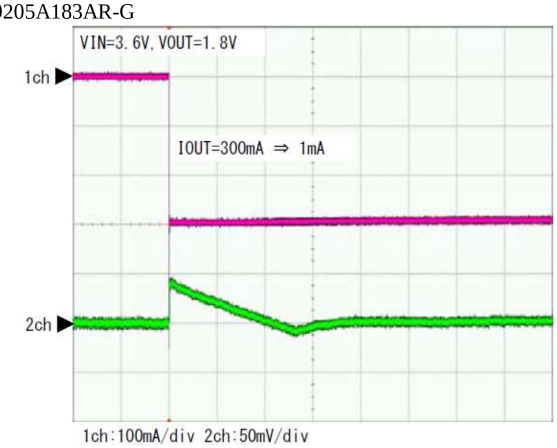
Ch1 – I_{OUT}, Ch2 – V_{OUT} 50 mV/div, Time – 100 µs/div

IXD9205A183AR-G



Ch1 – I_{OUT}, Ch2 – V_{OUT} 50 mV/div, Time – 100 µs/div

IXD9205A183AR-G



Ch1 – I_{OUT}, Ch2 – V_{OUT} 50 mV/div, Time – 100 µs/div

ORDERING INFORMATION

IXD9205①②③④⑤⑥-⑦ PWM Mode only
IXD9206①②③④⑤⑥-⑦ PFM/PWM Mode auto switching

DESIGNATOR	DESCRIPTION	SYMBOL	DESCRIPTION
①	Type of DC/DC Controller	A	$V_{IN} \geq 2$ V, No C_L auto discharge, standard soft start
		B	$V_{IN} \geq 2$ V, C_L auto discharge, fast soft start
		C	$V_{IN} \geq 2$ V, C_L auto discharge, standard soft start
		G	$V_{IN} \geq 1.8$ V, C_L auto discharge, fast soft start
②③	Fixed Output Voltage, V[2]	08 - 40	② - integer part, ③ - decimal part, i.e. V_{OUT} = 2.8 V - ② = 2, ③ = 8 0.05 V increments: 0.05 = A, 0.15 = B, 0.25 = C. 0.35 = D, 0.45 = E, 0.55 = F, 0.65 = H, 0.75 = K, 0.85 = L, 0.95 = M V_{OUT} = 2.85 V - ② = 2, ③ = L
④	Oscillation Frequency	3	3.0 MHz
⑤⑥-⑦[1]	Packages (Order Limit)	AR-G	CL-2025 (3000/reel)

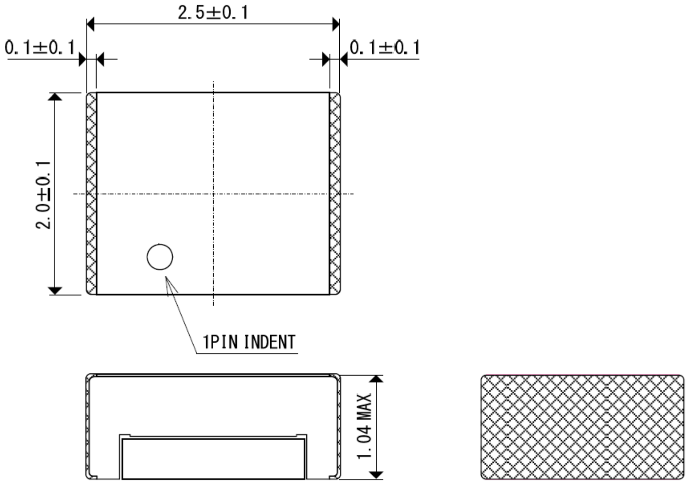
NOTE:

1) The “-G” suffix denotes halogen and antimony free, as well as being fully RoHS compliant.
2) Standard output voltages are: 1.0, 1.2, 1.4, 1.5, 1.75, 1.8, 1.9, 2.5, 2.8, 2.85, 3.0, and 3.3 V. Contact local representative for more information if other voltages in the range from 0.8 to 4.0 V require.

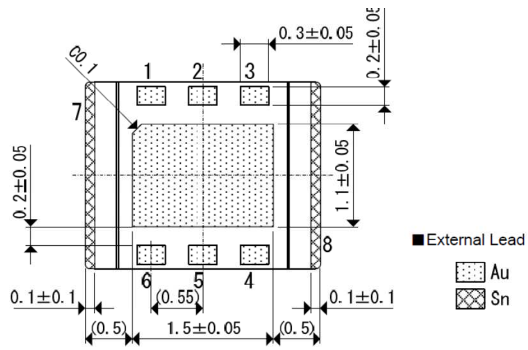
PACKAGE DRAWING AND DIMENSIONS

(Units: mm)

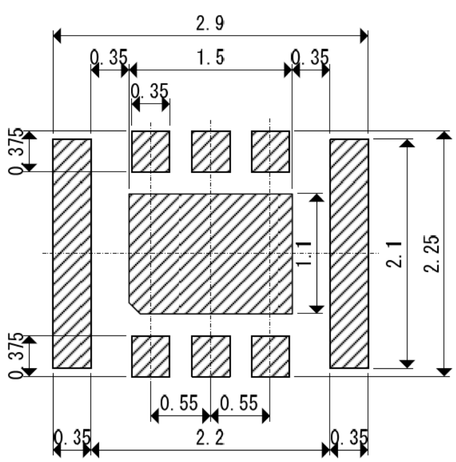
CL-2025



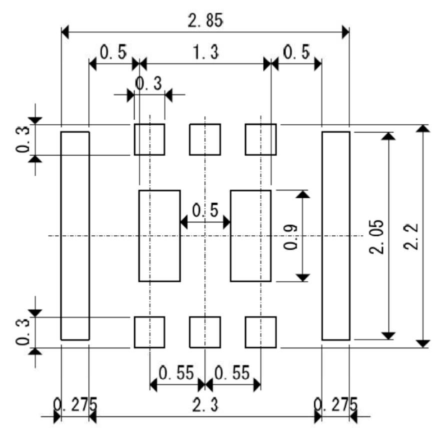
Bottom View



Reference Pattern Layout



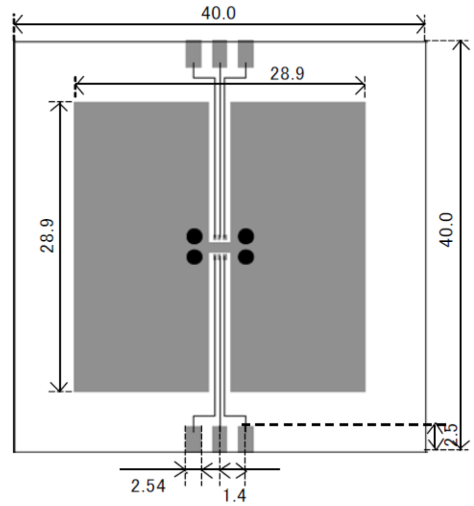
Reference Metal Mask Design



PACKAGE POWER DISSIPATION

CL-2025 Power Dissipation

The power dissipation varies with the mount board conditions. Please use this data as a reference only.



Evaluation Board (Unit: mm

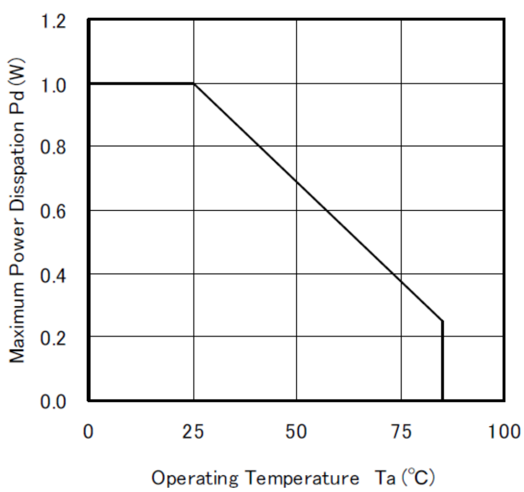
1. Measurement Conditions:

Condition:	Mount on a board
Ambient:	Natural convection
Soldering:	Lead (Pb) free
Board:	Dimensions 40×40 mm (1600 mm^2 in one side) Copper (Cu) traces occupy 50% of the board area on top and bottom layers Package heat sink tied to the copper traces.
Material:	Glass Epoxy (FR-4)
Thickness:	1.6 mm
Through-hole:	4 x 0.8 Diameter

2. Power Dissipation vs. Ambient Temperature

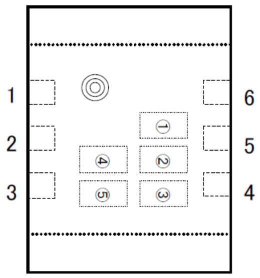
Board Mount (Tjmax = 125 ^{0}C)

Ambient Temperature, ^{0}C	Power Dissipation Pd, mW	Thermal Resistance, ^{0}C/W
25	1000	80
85	250	



MARKING

CL-2025



① Represents product series

MARK	PRODUCT SERIES
4	IXD9205Axxxxx-G
C	IXD9205Bxxxxx-G
	IXD9205Gxxxxx-G
K	IXD9205Cxxxxx-G
5	IXD9206Axxxxx-G
D	IXD9206Bxxxxx-G
	IXD9206Gxxxxx-G
L	IXD9206Cxxxxx-G

② Represents integral part of the voltage value

OUTPUT VOLTAGE, V	MARK	
	IXD920xA/B/C	IXD920xG
0.x	F	U
1.x	H	V
2.x	K	X
3.x	L	Y
4.x	M	Z

③ Represents decimal part of the Voltage value

V_{OUT}, V	MARK
x.00	0
x.05	A
x.10	1
x.15	B
x.20	2
x.25	C
x.30	3
x.35	D
x.40	4
x.45	E
x.50	5
x.55	F
x.60	6
x.65	H
x.70	7
x.75	K
x.80	8
x.85	L
x.90	9
x.95	M

④⑤ Represents production lot number
01～09、0A～0Z、11～9Z、A1～A9、AA～AZ、B1～ZZ in order
(G, I, J, O, Q, and W excluded)

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